

Electronic Acknowledgement Receipt

EFS ID:	4787528
Application Number:	10714935
International Application Number:	
Confirmation Number:	3704
Title of Invention:	Shift register block, and data signal line driving circuit and display device using the same
First Named Inventor/Applicant Name:	Kazuhiro Maeda
Customer Number:	23117
Filer:	MICHAEL J SHEA
Filer Authorized By:	
Attorney Docket Number:	1035-483
Receipt Date:	12-FEB-2009
Filing Date:	18-NOV-2003
Time Stamp:	22:35:32
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$880
RAM confirmation Number	4857
Deposit Account	141140
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)

1	Fee Worksheet (PTO-06)	1035-483-cvr.pdf	419472 eb3e263f51aa49a16c9f86db260a8ca887eb a70a	no	1
Warnings:					
Information:					
2		1035-483-amd.pdf	4799866 97884e4fb8a40e627e1811fad244ab62ed5 a3264	yes	17
Multipart Description/PDF files in .zip description					
Document Description			Start	End	
Amendment/Req. Reconsideration-After Non-Final Reject			1	1	
Claims			2	15	
Applicant Arguments/Remarks Made in an Amendment			16	17	
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	30077 7bc5943eaf7201d9d16cc68789d524e170 62fcf	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				5249415	
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